REMARKS

Applicant has carefully studied the outstanding Office Action in the present application. The present response is intended to be fully responsive to all points of rejection raised by the Examiner and is believed to place the application in condition for allowance. Favorable reconsideration and allowance of the application are respectfully requested.

Claims 1-11, 13-22, 24, 28-41 and 51 stand rejected under 35 USC 102(b) as being anticipated by Wingate. Claims 12, 23 and 42 stand rejected under 35 USC 103(a) as being unpatentable over Wingate. Claims 25-27 and 43-50 stand rejected under 35 USC 103(a) as being unpatentable over Wingate in view of the Applicant's admitted prior art.

The Examiner, concerning the 35 USC 102(b) rejection of independent claims 13, 34 and 51, inter alia, wrote "Wingate discloses circuitry for signal measurement comprising a signal input generated by sensors, a microprocessor (Fig. 1), and an oscillator, wherein the oscillator is operable to generate a pulse signal, the frequency of which is a function of amplitude of a first signal (Fig. 2B, Column 4, lines 49,52), and then supply the pulse signal to the microprocessor, and wherein the microprocessor is operable to measure the frequency of the pulse signal by comparing the pulse signal with a timing signal (Column 4, lines 53-56; Column 5, lines 4-15)."

Applicant respectfully disagrees with the Examiner's rejection of these claims. While applicant agrees that Wingate supplies a signal to the microprocessor, Wingate does not show or suggest providing a signal as an input to the microprocessor clock as claimed in independent claims 13, 34 and 51.

Independent claim 13 recites "a clock oscillator circuit operable to generate **a clock signal for said microprocessor**". Independent claim 34 also recites "a clock oscillator generating **a clock signal for said microprocessor**". Independent claim 51 similarly recites "providing a first signal to an oscillator circuit operable to generate **a clock signal for a microprocessor**". None of the prior art shows or suggests using the output of the oscillator as an input to the microprocessor clock as recited in independent claims 13, 34 and 51.

Additionally, independent claim 1 has been amended to recite this novel feature. Amended claim 1 recites "said oscillator being operable to generate a pulse signal ... and to supply said pulse signal to a clock input of said microprocessor". None of the prior art

shows or suggests using the output of the oscillator as an input to the microprocessor clock as recited in independent claim 1.

Independent claim 33 has also been amended to recite "a microprocessor having a clock input". None of the prior art shows or suggests providing an input to the microprocessor clock as recited in claim 33.

Accordingly, independent claims 1, 13, 33, 34 and 51 are deemed to be allowable. All of the remaining claims each depend directly or ultimately from one of these independent claims and recite additional patentable matter, and are therefore deemed allowable. Thus, all of the claims are deemed to be allowable.

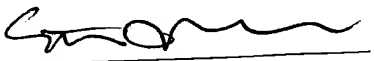
Additionally, minor amendments have been made to claim 5, 8, 11 and 12. No new matter has been added.

Applicant reserves the right to pursue the claims as filed in the context of a continuation application.

Applicant has carefully studied the remaining prior art of record herein and concludes that the invention as described and claimed in the present application is neither shown in nor suggested by the cited art.

In view of the foregoing remarks, all of the claims are believed to be in condition for allowance. Favorable reconsideration and allowance of the application is respectfully requested.

Respectfully submitted,



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